wherein a clock is output that has an operational frequency that is equal to that of the reference clock when serving as the non-operational mode or N times the reference clock when serving as the operational mode based on the result of the logical processing of said logic circuit.

REMARKS

Applicants respectfully request that the foregoing amendments to Claims 4 and 6 and new Claims 12-15 be entered in order to avoid this application from incurring a surcharge for the presence of one or more multiple dependent claims.

Respectfully submitted,

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Date

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